

WHAT IS CLAIMED IS:

1. A reflective liquid crystal display device, comprising:
 - first and second substrates spaced apart and facing each other;
 - a gate line and a data line on an inner surface of the first substrate and crossing each other to define a pixel area;
 - a thin film transistor corresponding to each pixel area electrically connected to the gate and data lines;
 - a passivation layer covering the thin film transistor;
 - a blocking layer on the passivation layer and corresponding to the thin film transistor;
 - a pixel electrode corresponding to each pixel area on the passivation layer and connected to the thin film transistor;
 - a retardation film on an outer surface of the first substrate;
 - a polarizer on the retardation film;
 - a color filter layer on the second substrate;
 - a common electrode on the color filter layer; and
 - a liquid crystal layer between the common electrode and the pixel electrode, wherein the blocking layer is made of a metal material.
2. The device according to claim 1, wherein the data line overlaps adjacent pixel electrodes and an overlapping width is between about 50 % and about 100% of a width of the data line.

3. The device according to claim 1, wherein a portion of the width of the data not overlapped by the adjacent pixel electrodes is less than about 50%.

4. The device according to claim 1, further comprising an absorption layer on an inner surface of the second substrate, wherein the color filter layer is a cholesteric liquid crystal color filter layer.

5. The device according to claim 1, further comprising an reflective layer on an inner surface of the second substrate, wherein the color filter layer is a absorption type color filter layer.

6. The device according to claim 1, wherein the blocking layer includes chromium.

7. The device according to claim 1, wherein at least a portion of the blocking layer is disposed between the passivation layer and the pixel electrode.

8. The device according to claim 1, wherein the blocking layer is disposed on the pixel electrode.

9. The device according to claim 1, wherein the passivation layer includes one of benzocyclobutene and acrylic resin.

10. A reflective liquid crystal display device, comprising:
 - first and second substrates spaced apart and facing each other;
 - a gate line and a data line on an inner surface of the first substrate and crossing each other to define a pixel area;
 - a thin film transistor electrically connected to the gate and data lines;
 - a passivation layer covering the thin film transistor;
 - a blocking layer on the passivation layer and corresponding to the thin film transistor;
 - first and second black matrices on the passivation layer and overlapping respective sides of the data line;
 - a pixel electrode on the passivation layer, the pixel electrode connected to the thin film transistor and overlapping the first and second black matrices;
 - a retardation film on an outer surface of the first substrate;
 - a polarizer on the retardation film;
 - a color filter layer on the second substrate;
 - a common electrode on the color filter layer; and
 - a liquid crystal layer between the common electrode and the pixel electrode.
11. The device according to claim 10, further comprising an absorption layer on an inner surface of the second substrate, wherein the color filter layer is a cholesteric liquid crystal color filter layer.

12. The device according to claim 10, further comprising an reflective layer on an inner surface of the second substrate, wherein the color filter layer is a absorption type color filter layer.
13. The device according to claim 10, wherein the pixel electrode overlaps adjacent data lines.
14. The device according to claim 10, wherein the first and second black matrices are made of the same material as the blocking layer.
15. The device according to claim 10, wherein the blocking layer is made of a metal material.
16. The device according to claim 15, wherein the blocking layer includes chromium.
17. The device according to claim 10, wherein the blocking layer is disposed between the passivation layer and the pixel electrode.
18. The device according to claim 10, wherein the blocking layer is disposed on the pixel electrode.
19. A reflective liquid crystal display device, comprising:

first and second substrates spaced apart and facing each other;
first and second black matrices on an inner surface of the first substrate;
a plurality of gate lines on the inner surface of the first substrate;
a plurality of data lines crossing the gate lines to define pixel areas and
overlapping the first and second black matrices;
a thin film transistor electrically connected to the gate and data lines;
a passivation layer covering the thin film transistor;
a blocking layer on the passivation layer and corresponding to the thin film
transistor;
a pixel electrode on the passivation layer, the pixel electrode connected to the
thin film transistor and overlapping the first and second black matrices;
a retardation film on an outer surface of the first substrate;
a polarizer on the retardation film;
a color filter layer on the second substrate;
a common electrode on the color filter layer; and
a liquid crystal layer between the common electrode and the pixel electrode.

20. The device according to claim 19, further comprising an absorption layer on
an inner surface of the second substrate, wherein the color filter layer is a cholesteric liquid
crystal color filter layer.

21. The device according to claim 19, further comprising an reflective layer on an inner surface of the second substrate, wherein the color filter layer is a absorption type color filter layer.

22. The device according to claim 19, wherein the pixel electrode overlaps adjacent data lines.

23. The device according to claim 19, wherein the first and second black matrices are made of the same material as the gate lines.

24. The device according to claim 19, further comprising an overcoat layer between the first and second black matrices and the gate lines.

25. The device according to claim 19, wherein the blocking layer is made of a metal material.

26. The device according to claim 19, wherein the blocking layer is disposed between the passivation layer and the pixel electrode.

27. The device according to claim 19, wherein the blocking layer is disposed on the pixel electrode.